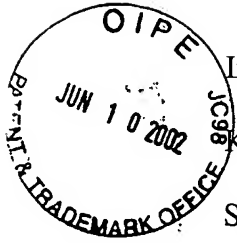


Docket No.: 61282-022

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



In re Application of

Kenji SHIMAZAKI, et al.

Serial No.: 10/092,737

Group Art Unit: 2857

Filed: March 08, 2002

Examiner:

For: ELECTROMAGNETIC DISTURBANCE ANALYSIS METHOD AND  
APPARATUS AND SEMICONDUCTOR DEVICE MANUFACTURING  
METHOD USING THE METHOD

*7/Priority  
Paper  
P. Walker  
4-3-03*

**TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT**

Honorable Commissioner for Patents and Trademarks  
Washington, D. C. 20231

Sir:

At the time the above application was filed, priority was claimed based on the  
following application:

**Japanese Patent Application No. P.2001-065105, filed March 8, 2001**

A copy of each priority application listed above is enclosed.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

*[Signature]*  
Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202)756-8000 MEF:mlw  
Facsimile: (202)756-8087  
**Date: June 10, 2002**